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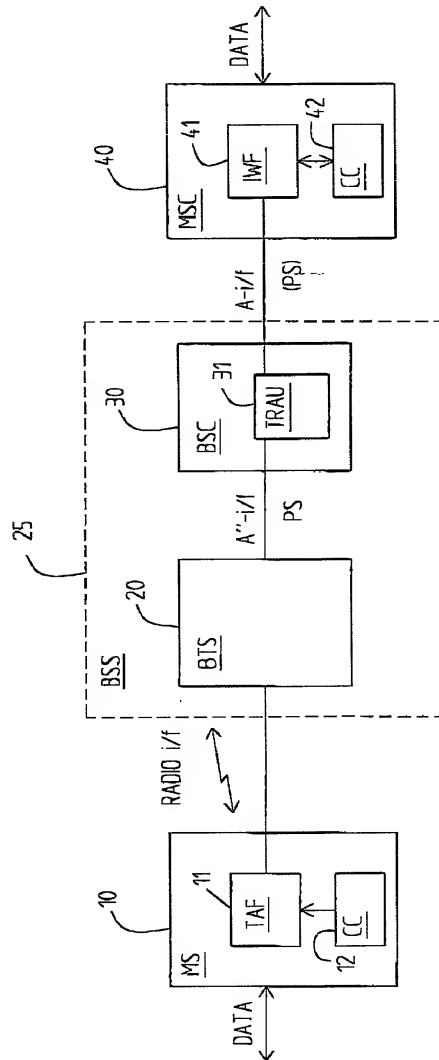


Fig. 1A

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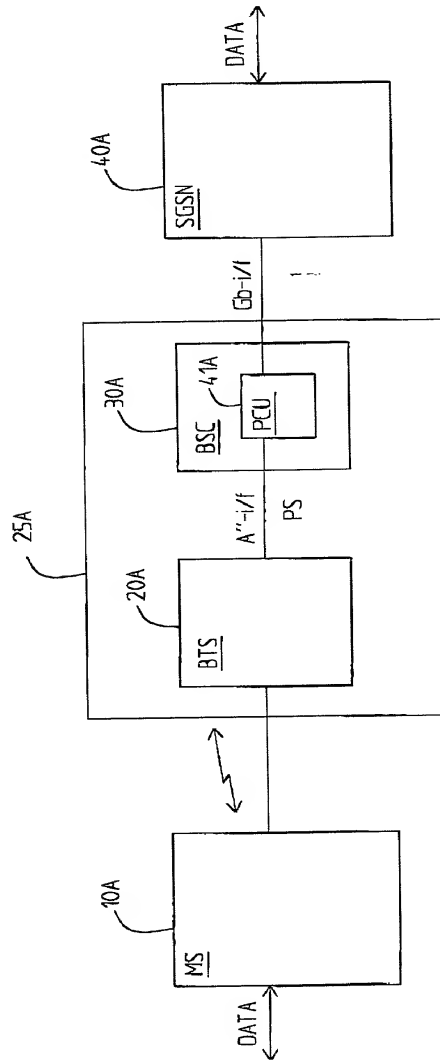


Fig. 1B

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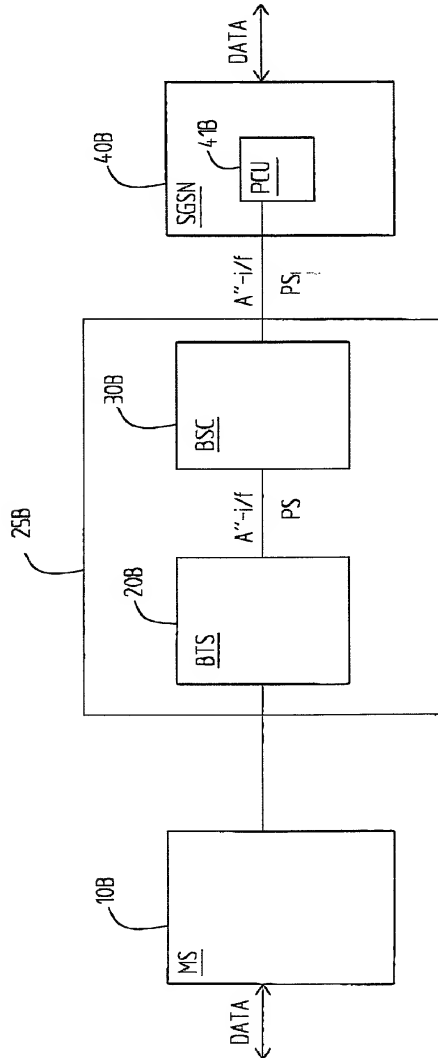


Fig. 1C

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Fig. 2

	8	7	6	5	4	3	2	1
0	SA	SB	X	0	0	0	1	1
1	1	1	0	0	0	1	1	1
2	1	1	0	1	0	0	1	1
3	1	1	0	0	1	1	0	1
4	SA	SB	X	1	1	1	1	1
5								
6								
7								
8								
9								
10								

Fig. 3

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	E2	E3
1ST MODIFIED CCITT V.110 80 BIT FRAME	0	0
2ND —————	0	1
3RD —————	1	0
4TH —————	1	1

Fig. 4A

OCTET NO.	BIT NUMBER							
	0	1	2	3	4	5	6	7
0	0	0	0	0	0	0	0	0
1	1	D1	D2	D3	D4	D5	D6	D'1
2	1	D7	D8	D9	D10	D11	D12	D'2
3	1	D13	D14	D15	D16	D17	D18	D'3
4	1	D19	D20	D21	D22	D23	D24	D'4
5	1	E1	E2	E3	D'5	D'6	D'7	D'8
6	1	D25	D26	D27	D28	D29	D30	D'9
7	1	D31	D32	D33	D34	D35	D36	D'10
8	1	D37	D38	D39	D40	D41	D42	D'11
9	1	D43	D44	D45	D46	D47	D48	D'12

Fig. 4B

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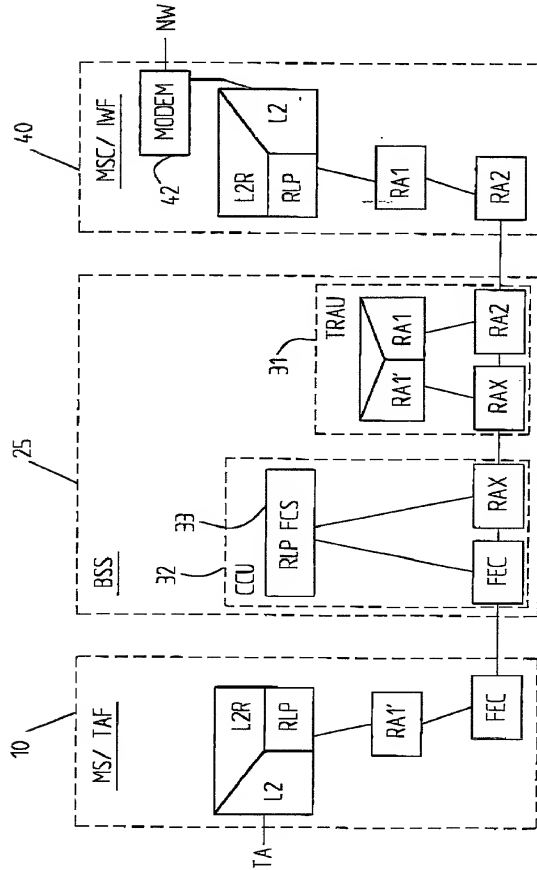


Fig. 5

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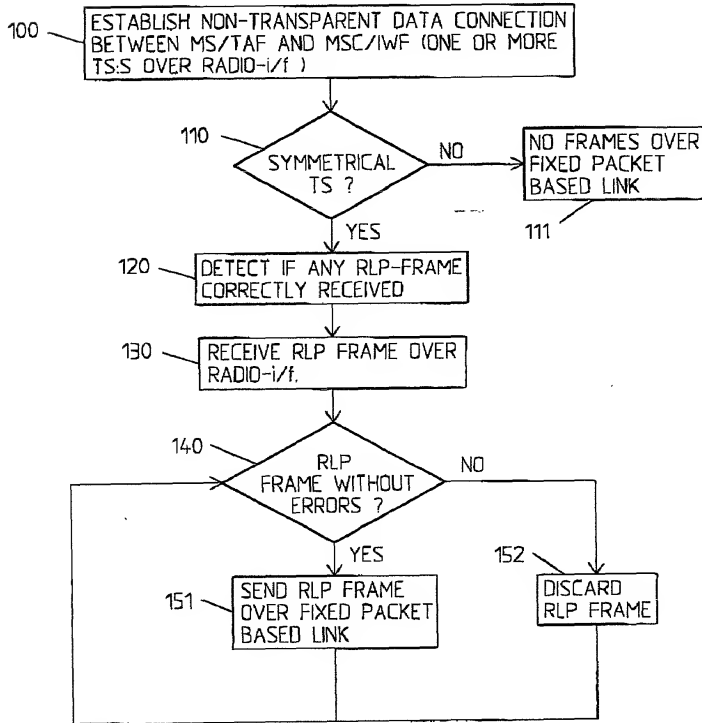


Fig. 6

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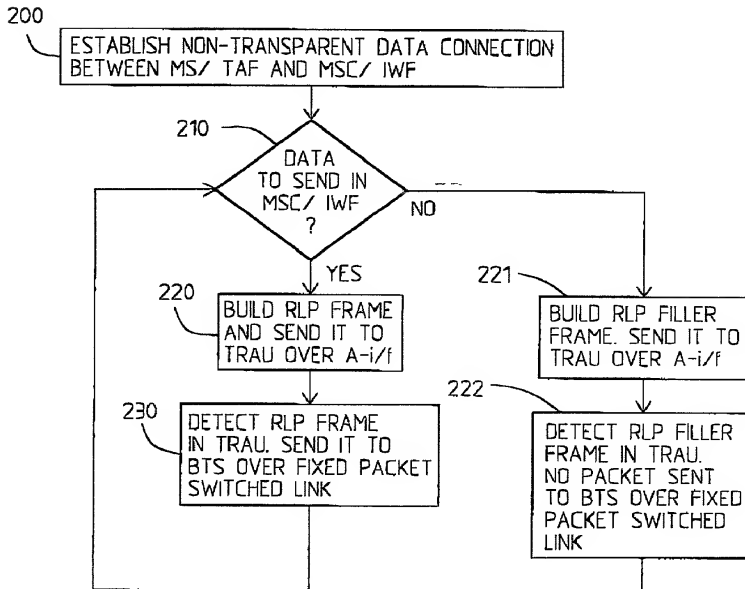


Fig. 7

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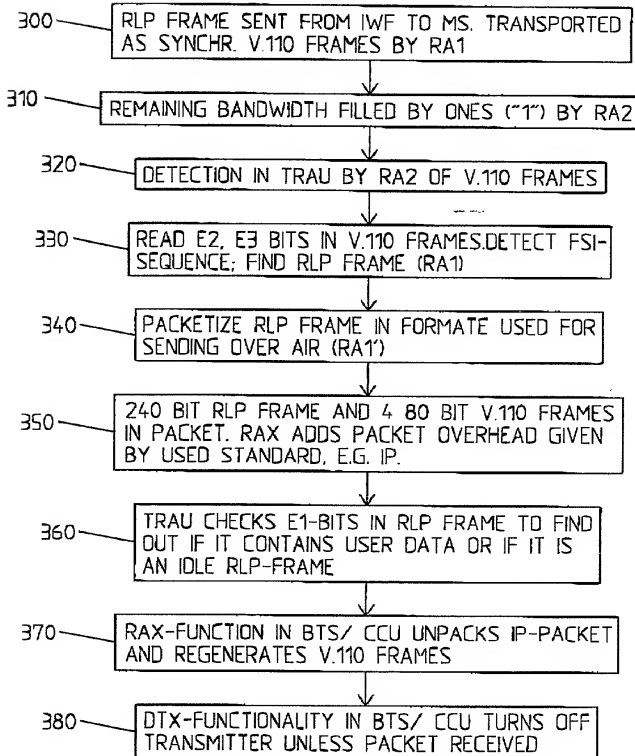


Fig. 8